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01 FC:1501 02 FC:1504	1400.00 ( 300.00	nn /4 »	O OFFE		4	130/01	(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED IN	/ENTOR	$\overline{}$	ATTORNEY DOCKET NO.	CONFIRMATION NO.
L							
10/731,269	10/731,269 12/08/2003		Anand Murthy			42P17811	4724
TITLE OF INVENTION:	METHOD FOR IMPROVING	G TRANSISTOR P	ERFORMANCE	THROUGH REDU	JCING T	HE SALICIDE INTERFAC	CE RESISTANCE
APPLN. TYPE	SMALL ENTITY	ISSUE F	EE	PUBLICATION FE	E	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	nonprovisional NO \$		00 \$300			\$1700	07/01/2005
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EXAMINER		ART UNIT		CLASS-SUBCLASS	s		
NHU, DAVID		2818		438-933000			
1. Change of corresponder	ice address or indication of "F	ee Address" (37	2. For printing	on the patent front	page, list	DIAVELL	
CFR 1.363).	1 11 ( 61 6		(1) the names	of up to 3 registere	ed patent	augmeys -	Y, SOKOLOFF,
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INTEL CORPORATION SANTA CLARA, CALIFORNIA							
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a. Applicant claims	SMALL ENTITY status. See	37 CFR 1.27.	b. Applicant is	s no longer claimin	g SMAL	L ENTITY status. See 37 (	CFR 1.27(g)(2).
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